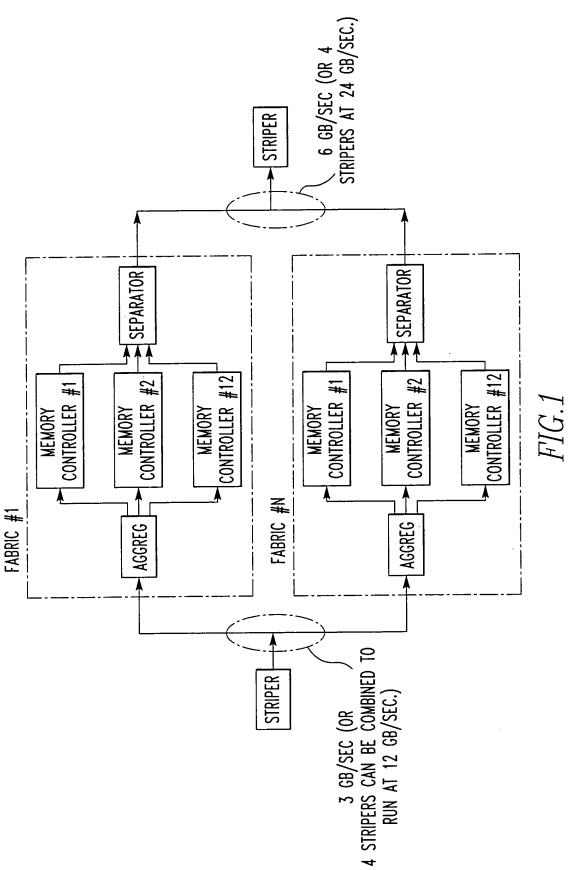
Replacement Floury
1/8





## REPLACEMENT FIGURE

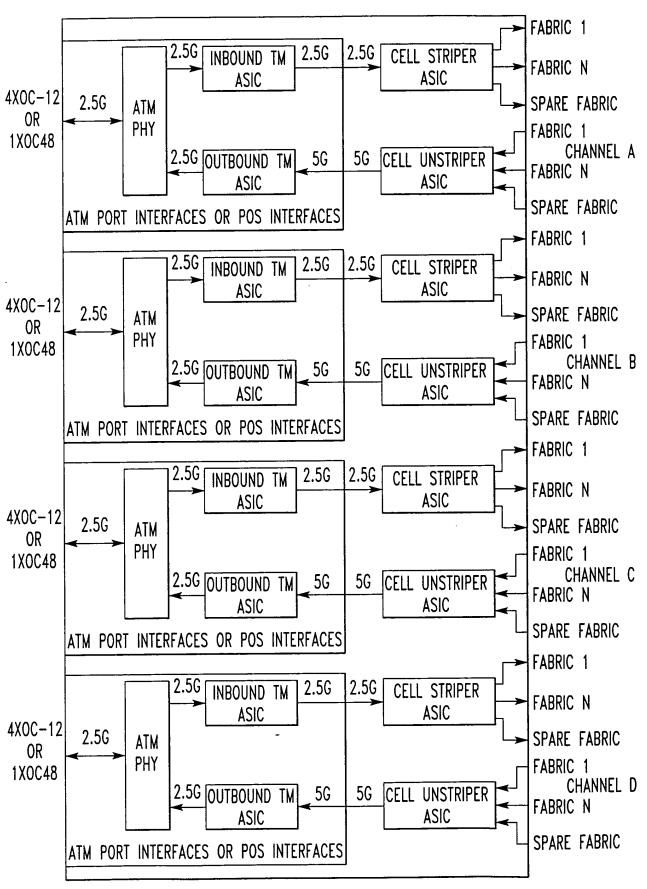


FIG.2

## REPLACEMENT FIGURE

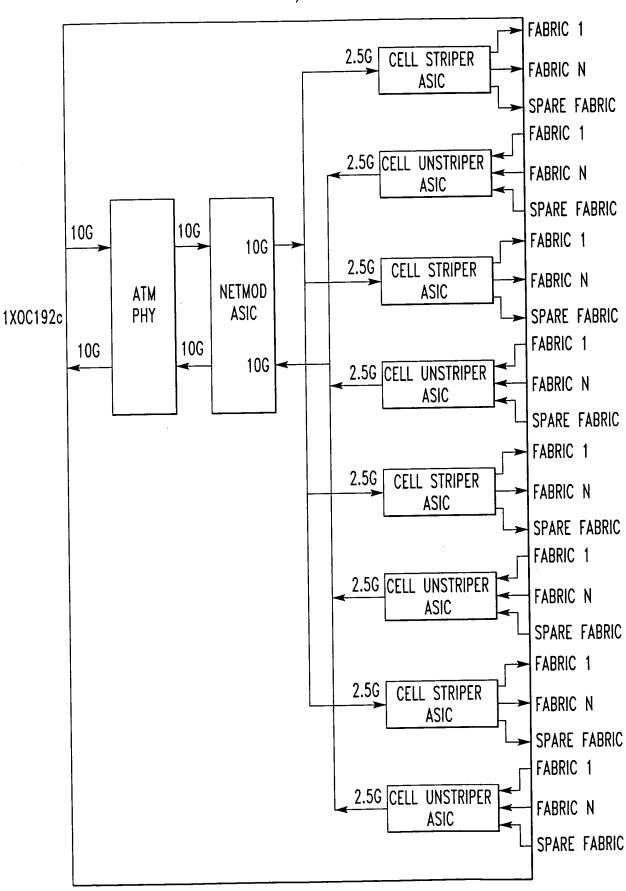
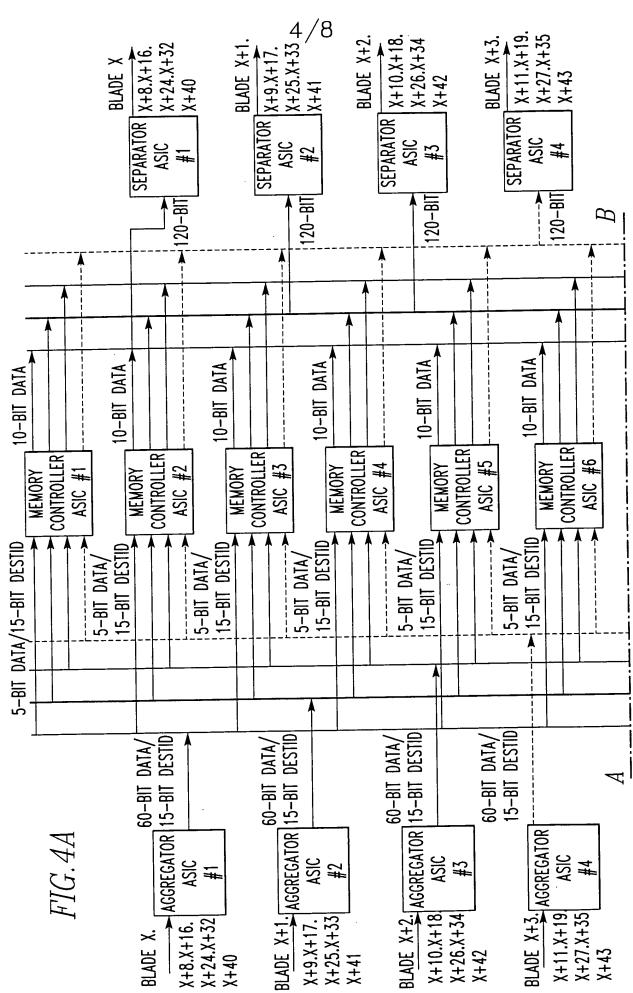


FIG.3



## FIGURE PLACEMEN SEPARATOR X+14.X+22.C1 ASIC X+30.X+38 X+46 | SEPARATOR | X+13.X+21. | ASIC | X+29.X+37 | #6 | X+45 | X+15.X+23. | X+31.X+39 | X+47 BLADE X+7. BLADE X+6. BLADE X+5. BLADE X CONTROL BLADES PORT SEPARATOR SEPARATOR ASIC #9 ASIC #8 120-BIT 20-BT 120-BIT 120-BIT 10-BIT DATA 10-BIT DATA 10-BIT DATA 10-BIT DATA 10-BIT DATA CONTROLLER CONTROLLER CONTROLLER CONTROLLER CONTROLLER ASIC #12 ASIC #10 MEMORY MEMORY ASIC #8 ASIC #11 MEMORY MEMORY MEMORY ASIC #8 15-BIT DESTID 15-BIT DESTID 15-BIT DESTID 15-BIT DESTID 15-BIT DESTIQ 5-BIT DATA, 5-BIT DATA 5-BIT DATA, 5-BIT DATA 5-BIT DATA, 60-BIT DATA/ 15-BIT DESTID 60-BIT DATA/ 15-BIT DESTID 60-BIT DATA/ 15-BIT DESTID 60-BIT DATA/ 15-BIT DESTID 15-BIT DESTID 60-BIT DATA/ 8# SN8 | BUS #9 BUS #6 BUS #7 BLADE X+7- AGGREGATOR BLADE X+6. AGGREGATOR AGGREGATOR BLADE X+5-AGGREGATOR ASIC ASIC #9 **ASIC** ASIC #6 ASIC #5 **L**# 8

X+31.X+39 X+47

CONTROL

BLADES PORT

X+15.X+23

X+14.X+22 X+30.X+38

X+46

X+29.X+37 X+13.X+21

X+45

X+28.X+36

ASIC #5

120-BIT

X+44

X+12.X+20

SEPARATOR

 $\mathcal{B}$ 

10-BIT DATA

5-BIT DATA/15-BIT DESTID

CONTROLLER

BLADE X+4. AGGREGATOR BUS #5

X+12.X+20 X+28.X+36

X+44

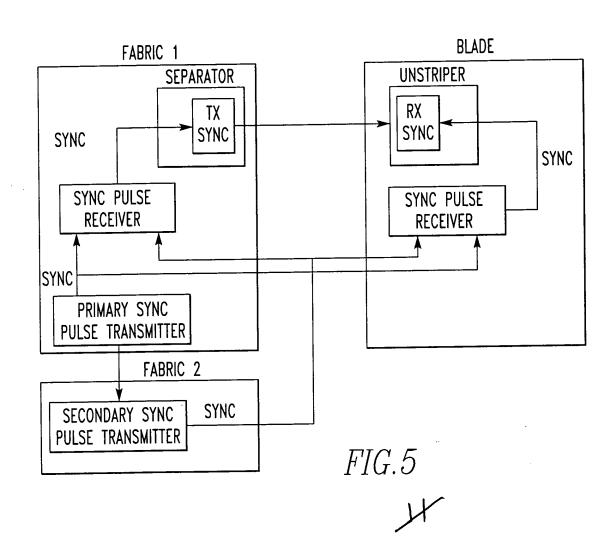
FIG.4B

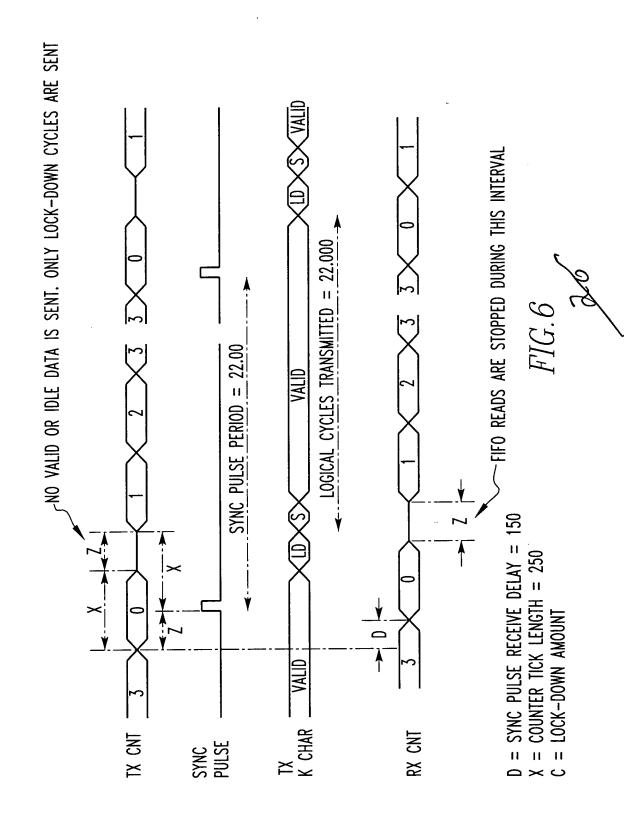
MEMORY

ASIC #7

BLADE X+4.

SUBSTITUTE FIGURE





(B)

SUBSTITUTE FIGURE

8/8

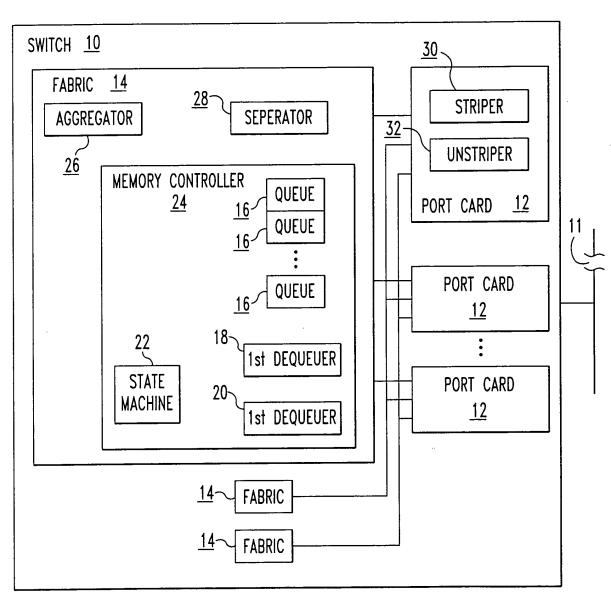


FIG.7